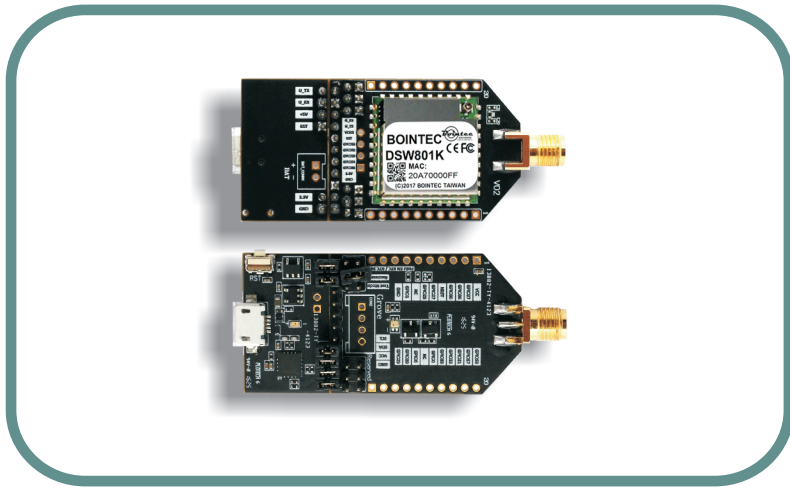




# DEV801K

## DSW801K Evaluation Board



DSW801K Evaluation Board



### Introduction of Products

DEV801K is a low-cost and easy to use Internet of Things (IoT) development platform for RTOS to design, prototype, evaluate and implement IoT projects. It is based on DSW801K stamp Module, with ARM Cortex-M4 with floating point MCU in package. The HDK enables rich connectivity features, communication with cloud services and real-time control.

### Product Highlight

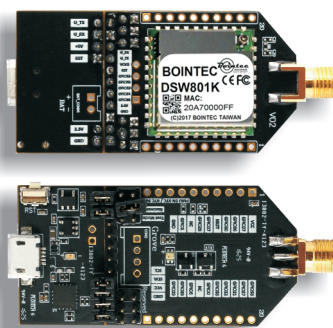
- ARM Cortex M4 MCU with FPU with up to 192MHz clock speed
- Embedded 352KB SRAM and 64KB boot ROM Internal 2MB Flash
- Interface (QPI) mode
- Supports eXecute In Place (XIP) on flash
- 32KB cache in XIP mode
- Two UART interfaces with hardware flow control and one UART for debug, all multiplexed with GPIO
- One SPI slave interface multiplexed with GPIO
- Two I2C master interface multiplexed with GPIO
- One I2S interface multiplexed with GPIO
- Four channel 12-bit ADC multiplexed with GPIO
- IEEE 802.11 b/g/n compliant
- Supports 20MHz,40MHz bandwidth in 2.4GHz
- Dual-band 1T1R mode with data rate up to 150Mbps



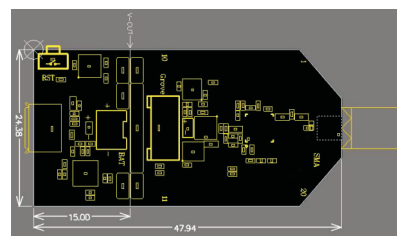
# Specification

Chipset	MT76X7 series
Core	ARM Cortex-M4 MCU
FPU Clock Speed	192MHz
SRAM	352KB
Flash	Internal 2MB / external 4MB / external 4MB
Power	5V / 3.3V / Li-ion Battery
Humidity	Operating : Operating : -20°C ~ +70°C (Non-Condensing)
	Storage : -40°C ~ +85°C (Non-Condensing)
Dimension	57mm X 24.5mm X 15mm (Typ.)
Package	PCBA

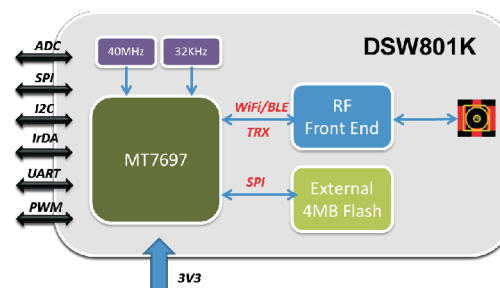
## Product quick glance



## ME Drawing/placement



## Block Diagram



## Ordering Information

PART NUMBER	DESCRIPTION
TFGA-DEV80100-11	DSW801K Evaluation Board



We are Your Partner more than Business



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